Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	102	717/150.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 14:18
L2	165	717/160.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 14:18
L3	234	1 2	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 14:18
L4	1	3 and cache near (synonym or alias\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 14:19
S1	427	717/150.ccls. 717/159-161.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 14:17
S2	7080	prefetch pre adj fetch	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S3	2008	(detail section) adj loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S4	638	loop near5 (unroll\$3 un adj roll\$3 restructur\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S5	56	(loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S6	28	cache adj management adj instruction	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S7	10	(prefetch pre adj fetch) and (cache adj management adj instruction)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42

S8	4056428	code loop source object	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S9	166	((prefetch pre adj fetch cache adj management) adj instruction) near3 (source code loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S10	4	((loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)) and ((detail section) adj loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:42
S11	1	((memory cache) near3 thrashing) and loop near (distribution unroll un adj roll roll)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S12	9	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (code loop source object)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S13	35	((prefetch pre adj fetch cache adj management) adj instruction) near3 (loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S14	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S15	325	(memory cache) near3 thrashing	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S16	1	((memory cache) near3 thrashing) and loop near distribution	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S17	136	((memory cache) near3 thrashing) and loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S18	14	((memory cache) near3 thrashing) and loop near (distribution unroll\$3 un adj roll\$3 roll\$3 optimiz\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/07/08 17:43
S19	2	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (loop near5 (unroll\$3 un adj roll\$3 restructur\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/01/29 13:47

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S42	5	("5797013" "5704053" "6539541" "5862385" "5950007").pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/04 14:30
S43	1	"6675374".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/04 14:30
S44	0	"09416436".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:43
S45	464	717/150.ccls. 717/159-161.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S46	7681	prefetch pre adj fetch	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S47	2142	(detail section) adj loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S48	691	loop near5 (unroll\$3 un adj roll\$3 restructur\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S49	61	(loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S50	32	cache adj management adj instruction	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S51	12	(prefetch pre adj fetch) and (cache adj management adj instruction)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S52	4275424	code loop source object	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S53	182	((prefetch pre adj fetch cache adj management) adj instruction) near3 (source code loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44

S54	4	((loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)) and ((detail section) adj loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S55	1	((memory cache) near3 thrashing) and loop near (distribution unroll un adj roll roll)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S56	11	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (code loop source object)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S57	39	((prefetch pre adj fetch cache adj management) adj instruction) near3 (loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S58	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S59	362	(memory cache) near3 thrashing	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S60	1	((memory cache) near3 thrashing) and loop near distribution	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S61	154	((memory cache) near3 thrashing) and loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S62	16	((memory cache) near3 thrashing) and loop near (distribution unroll\$3 un adj roll\$3 roll\$3 optimiz\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S63	2	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (loop near5 (unroll\$3 un adj roll\$3 restructur\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S64	5	("5797013" "5704053" "6539541" "5862385" "5950007").pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44
S65	1	"6675374".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/01/24 07:44

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S66	496	717/150.ccls. 717/159-161.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S67	8219	prefetch pre adj fetch	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S68	2287	(detail section) adj loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S69	740	loop near5 (unroll\$3 un adj roll\$3 restructur\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S70	67	(loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S71	36	cache adj management adj instruction	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S72	14	(prefetch pre adj fetch) and (cache adj management adj instruction)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S73	4483130	code loop source object	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S74	200	((prefetch pre adj fetch cache adj management) adj instruction) near3 (source code loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S75	5	((loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)) and ((detail section) adj loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S76	1	((memory cache) near3 thrashing) and loop near (distribution unroll un adj roll roll)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S77	13	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (code loop source object)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23

S78	45	((prefetch pre adj fetch cache adj management) adj instruction) near3 (loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S79	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S80	393	(memory cache) near3 thrashing	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S81	1	((memory cache) near3 thrashing) and loop near distribution	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S82	168	((memory cache) near3 thrashing) and loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S83	16	((memory cache) near3 thrashing) and loop near (distribution unroll\$3 un adj roll\$3 roll\$3 optimiz\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S84	2	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (loop near5 (unroll\$3 un adj roll\$3 restructur\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S85	5	("5797013" "5704053" "6539541" "5862385" "5950007").pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S86	1	"6675374".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S87	0	"09416436".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S88	496	717/150.ccls. 717/159-161.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S89	8219	prefetch pre adj fetch	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23

S90	2287	(detail section) adj loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S91	740	loop near5 (unroll\$3 un adj roll\$3 restructur\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S92	67	(loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S93	14	(prefetch pre adj fetch) and (cache adj management adj instruction)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S94	4483130	code loop source object	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S95	200	((prefetch pre adj fetch cache adj management) adj instruction) near3 (source code loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S96	1	((memory cache) near3 thrashing) and loop near (distribution unroll un adj roll roll)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S97	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S98	1	((memory cache) near3 thrashing) and loop near distribution	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S99	1	"6675374".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 0	36	cache adj management adj instruction	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 1	5	((loop near5 (unroll\$3 un adj roll\$3 restructur\$3)) and (prefetch pre adj fetch)) and ((detail section) adj loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23

S10 2	13	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (code loop source object)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 3	45	((prefetch pre adj fetch cache adj management) adj instruction) near3 (loop)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 4	16	((memory cache) near3 thrashing) and loop near (distribution unroll\$3 un adj roll\$3 roll\$3 optimiz\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 5	2	((prefetch pre adj fetch) and (cache adj management adj instruction)) and (loop near5 (unroll\$3 un adj roll\$3 restructur\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 6	5	("5797013" "5704053" "6539541" "5862385" "5950007").pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ÓИ	2005/07/22 13:23
S10 7	168	((memory cache) near3 thrashing) and loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 8	393	(memory cache) near3 thrashing	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/07/22 13:23
S10 9	336	detail adj loop	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:16
S11 3	1048	loop near distribut\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:17
S11 5	272	S113 and (compiler or optimiz\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:17
S11 6	64	S113 and cache	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:59
S11 7	54	S115 and cache	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:42

S11 8	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 12:27
S12 0	1	"6029225".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:32
S12 1	1	"20020199178".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:53
S12 2	853	strip adj min\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:54
S12 3	1	S122 and S109	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:54
S12 4	10	S122 and S113	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:54
S12 5	46	S122 and cache	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:54
S12 6	48	S122 and cach\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:55
S12 7	1	S126 and synonym	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:55
S12 8	6	S126 and (synonym or alias\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/05/24 13:55

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distribution [18] have been performed as a pre-pass, so that a. loop-carried ... with

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narrow rectangular s cti n loop. The raised bevel had. scallop-edged shoulders, and

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third step is loop distribution. Finally, the last step prop-. agates constants into loop

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lo p distribution. In all of these models, the transforma-. tion is represented by a sequence ... loop nests. The difference in the cache misses is used to ...

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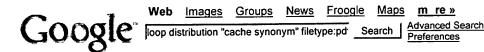
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Moreover, eco-driving, a synonym for safe driving, contributes to accident prevention

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double loop, and larvae have six pairs of ectodermal ampullae. ... junior synonym of

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example in documentation, to indicate invalid names, or as a synonym. for the loop

back address. Test and experimental software can escape ...

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used as a syn nym for a transit network domain, which provides a ... to the network

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distribution that consists of one value with 100% probability. dice :: Int -> Dist [Die] ... The loop was found to be non-significant. Since we were using ...

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be a synonym of C. alternans by Freeman (1961), but C. alternans as defined ... form

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Cache or was recruited from Grande Cache, Lassume, by. 5. Bob Parry. He was a

mechanical apprentice, the same as. 6. myself, when he arrived at the mine. ...

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with aliases (sometimes called syn nyms); ...

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LOGSTDBY status: ORA-16111: log **mining** and apply setting up ... setting the **Strip** HTML attribute on the Report Attribute page to No. ...

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mining. • timing for vector instructions. • chaining and convoys ... www.cis.upenn.edu/~amir/cis501-01/lecture_notes/review.pdf

[PDF] Coupling Compiler-Enabled and Conventional Memory Accessing for ...

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formations such as adaptive strip-mining [Moritz et al. 1999], our approach ... Other

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... program transformations such as adaptive strip-. mining [Moritz et al. ... Other

hardware based techniques to deal with the synonym problem are based ...

www.ecs.umass.edu/ece/andras/SSA/ssa4/papers/tocs281.pdf

[PDF] Design of the Transform Phase of the Digital Fortran Compiler

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Fortran compiler, later inherited by the Compaq and HP Fortran ... process, performs

strip mining and loop optimizations, vectorizes data motion, ...

www.hpl.hp.com/techreports/2005/HPL-2005-178.pdf

[PDF] Intel(R) Fortran Compiler for Linux* User's Guide Vol II

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The synonym of -ansi_alias is -assume [no]dummy_aliases. Alignment Options ...

strip mining. An optimization in which the compiler creates an additional ...

www.ncsa.uiuc.edu/UserInfo/Resources/Software/Intel/Compilers/8.1/for_ug2.pdf

[PDF] Intel® Fortran Compiler for Linux* Systems User's Guide Volume II ...

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syn nyms for the Intel Fortran Compiler back-end optimizations. ... Strip-mining,

also known as loop sectioning, is a loop transformation technique ...

www.ae.utexas.edu/lrc/fortran/intel/for_ug2.pdf

[PDF] 152 rabies . An infectious disease of the central nervous system ...

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Syn nym: abseil. See also. abseil; carabiner. rappel rack. A long U-shaped steel bar

... effects such as strip mining. recorder. An instrument designed to ...

genet-server.ibtm.tuwien.ac.at/speleo/pdf/glossary_02_R.pdf

[PDF] 162 sabath . See hardpan, nari. safe yield . The amount of water ...

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Synonym for a sump, or a section. of flooded cave passage, in common. parlance. ...

surface mine. Strip mine. [16] . surface retention. Water held on land ...

genet-server.ibtm.tuwien.ac.at/speleo/pdf/glossary_02_S.pdf

[PDF] A Partitioning Compiler for Computers with Xputer-based Accelerators

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An example for strip mining is given in figure 3-15, where as ... Computing Devices is

almost a syn nym of Hardware/Software Co-Design. The main ...

xputers.informatik.uni-kl.de/papers/publications/BeckerDiss.pdf

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Strip-mining and Cleanup lead the compiler to generate incorrect code. The synonym of -ansi_alias is -assume [no]dummy_aliases. Alignment Options ...

www.rz.uni-karlsruhe.de/rz/docs/HP-XC/intel-compilers/fc_8.1/for_ug2.pdf

[PDF] Programming Contests, Algorithms, and the Real World Steven Skiena ...

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It is NP-complete to minimize the sequential strip decompo-. sition for general models.

... We say two entities belong in the same syn nym set if: (1) ...

www.cs.sunysb.edu/~skiena/talks/topcoder.pdf

[PDF] Intel Fortran Compiler Optimizing Applications

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Synonym:. /unroll[:n]. -. unrolln. Specifies the maximum number of times you want to

... strip mining. An optimization in which the compiler creates an ...

www.ccs.usherbrooke.ca/downloads/compilateurs/Intel_V9_optaps_for.pdf

[PDF] 1.0 GLOSSARY

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ling the overburden material of strip mine banks. The. moving of soil particles closer

together by ... the static level (synonym of groundwater surface) of ...

www.luscar.com/pdf/bowcity_glossary.pdf

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metric could allow a compiler to produce appropriate. code for its manipulation. ...

torization, parallelization, and strip mining. IBM's ...

www.snakeisland.com/aplhiper.pdf

[PDF] LNAI 2700 - Terminology Mining

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tuted by one of their synonyms without modifying the syntactic structure. ... e, we

should strip this ending from it and append the string e to the stem. ...

www.springerlink.com/index/JK4KBK2YLPU0KWLV.pdf

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specialization and synonym relations. The relations are weighted (so called term ...

Eg name equality is used in the MODULA-2 compiler to ...

143.205.180.128/Publications/Dissertations/pubfiles/pdffiles/2001-0056-PH.pdf

[PDF] Acronyms, Abbreviations, and Terms

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synonym for semiannual). biaxial. bibliographic retrieval system ... strip-chart

recorder. strip-mine (v);. strip mine (n, adj). strip miner ...

mydocs.epri.com/docs/CorporateDocuments/WorkingWithEPRI/Terminology.pdf

[PDF] Some Industrial Mineral Deposit Models: Descriptive Deposit Mo

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Deposit synonyms: Marine evaporites, altered marine and rift-valley evaporites. ...

Maximum limitation of overburden: Most gypsum is mined by strip mining ...

pubs.usgs.gov/of/1991/ofr-91-0011-a/ofr-91-0011a.pdf

[PDF] PCS 5702 - Arquitetura de Computadores AULA 9 CMPE382 Fall 2002 ...

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("Strip Mining the Cache"). /* After */. for (jj = 0; jj < N; jj = jj+B) ... Dealing with aliases

(sometimes called synonyms); ...

regulus.pcs.usp.br/~pcs5702/pdf/aula9e10.pdf

[PDF] PCS 5702 - Arquitetura de Computadores AULA 6 CMPE382 Fall 2002 ...

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("Strip Mining the Cache"). /* After */. for (jj = 0; jj < N; jj = jj+B) ... The compiler can

insert prefetch instructions to request the data ...

regulus.pcs.usp.br/~pcs5702/pdf/aula6.pdf

[PDF] About Intel(R) C++ Compiler

File Format: PDF/Adobe Acrobat

The compiler automatically strip-mines your loop and generates a cleanup loop. ...

Strip Mining and Cleanup Loops. Statements in the Loop Body ...

www.tacc.utexas.edu/resources/userguides/intel/c_ug_lnx.pdf

[PDF] ARIZONA GAME AND FISH DEPARTMENT HERITAGE DATA MANAGEMENT SYSTEM ...

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plant in clay soils, and may be of considerable value revegetating **mine** spoils and ... LAND MANAGEMENT/OWNERSHIP: BLM – Arizona **Strip** Field Office; Private. ... www.gf.state.az.us/w_c/edits/documents/Artepygm.d_000.pdf

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www-static.cc.gatech.edu/classes/AY2006/cs6452_spring/slides/week12.pdf

(PDF) Typing Haskell in Haskell

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problems of interfacing a typechecker with **compiler** front ... do this because Haskell prevents the use of a **synonym** with- ...

www.cse.ogi.edu/~mpj/thih/thih-sep1-1999/thih.pdf

[PDF] FORGE

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A synonym for androgyne; now little used, but it's in the ... The compiler of this Dictionary thinks of zirself less as a com-. bination of male and female, ... www.forge-forward.org/newsletters/pdf/200005-GQ-Spirit-SM-news.pdf

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At the time of writing, running the Pan compiler requires having the ... As a first example, Figure 7.1 shows an infinitely tall vertical **strip** of unit ... conal.net/papers/functional-images/fop-conal.pdf

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support and new compiler optimization techniques •Design ...

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[PDF] Diploma Thesis

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simplest one: time, which is often more accurately denoted by **synonyms** like ... **strip mining** once again, the same reasons for doing so as in the matrix ...

www.se.eecs.uni-kassel.de/se/fileadmin/pm/publications/suess/thesis.pdf

[PDF] GNU Libtool

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program that will **strip** only debugging and **compiler** symbols from the library. ... **mine** whether shared or static libraries will be built. ... www.scs.stanford.edu/~reddy/links/gnu/libtool.pdf

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used the **synonym** of an envelope containing data - the IP header was the ... First off you need to **strip** the main insulation off about an inch from the ...

www.antionline.com/newsletter/aonewsletter1.pdf

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strip of galley proofs, a set of blank layout pages,. PMT's of illustrations, an X-acto

knife, ... terms), as well as related terms and synonyms, are ...

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[PDF] PGI 5 User s Guide

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as matrix multiply, using multi-dimensional **strip-mining** techniques to maximize ... the **c** mpiler treats "*" as a **syn** nym for standard input for reading and ...

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Synonyms: (British.) potholing;. spelunking. 2. A method of mining in ... effects such as strip mining. recorder. An instrument designed to ...

www.karstwaters.org/files/glossary.pdf

IPDF1 Methods for Assessing Biological Integrity of Surface Waters in ...

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strip mining, as well as crop and livestock production, have impacted stream water ...

Mills, MR compiler. 1988. Fish collection catalogue of the Kentucky ...

www.water.ky.gov/NR/rdonlyres/

E6B7C54C-CC63-46AA-B9ED-7ED9F099D5BB/0/SOP_Manual1.pdf

[PDF] NeTraMet & NeMaC

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The SRL compiler produces flow data files which NeMaC can read and download to a ... versions NeMaC treats Pushto and PushtoAct as synonyms for ...

www.auckland.ac.nz/net/Accounting/ntmref.pdf

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Current strip-mining. technology practiced in West German brown coal fields is ...

directly from the mine or plant to the final compiler.... govinfo.library.unt.edu/ota/Ota_5/DATA/1977/7703.PDF

[PDF] Oracle Database Readme

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LOGSTDBY status: ORA-16111: log mining and apply setting up ... setting the Strip

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[PDF] Distribution Restriction Statement

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Strip mining can indirectly affect the coast due, to increased erosion, which increases

sediment ... The term beach is sometimes used as a synonym for ... www.usace.army.mil/usace-docs/eng-manuals/em1110-2-1810/entire.pdf

[PDF] Fortran Programming Guide

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The f95 compiler also provides a Fortran 77 compatibility mode that accepts most ...

Strip-mining is no longer appropriate with modern compilers; ...

www.fzu.cz/oddeleni/savt/luna/dokumentace/Progrguide.pdf

[PDF] Cover.pd (Page 1)

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by mining. It has even been incorporated into poultry feed. Aqua- ... buffer strip,

vegetative. A gently sloping area of vegetation that ...

www.aqua.wisc.edu/publications/PDFs/AquacultureEffluents.pdf

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world history: a long, warm, standing ovation for the compiler of. a dictionary." ... or

Russian synonyms, which you can then look up in a bidirec- ...

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4.0 compiler optimized for G4. and G5 processors, and support. 64-bit development.

... Nouns, synonyms and qualifiers are added to create context. ...

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bookkeeping application for yourself and your bank balance (like min) sits ... explicitly defined at compile time (or the c mpiler will throw an error). I ...

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compiler into enzymes, which correspond, to the machine code. ... **synonyms**, the NLU unit constructs a frame-based semantic structure [36,37,38,39], which ... agents.media.mit.edu/projects/iseek/ashwani-ms.pdf

[PDF] Vol. 27/2 3/13/02

File Format: PDF/Adobe Acrobat - <u>View as HTML</u> gold mine of etymological information, thanks in part to the. eminent Yale philologist William Dwight ... compiler of the Dictionary of Underworld Slang ... www.verbatimmag.com/27_2.pdf

[PDF] Occasional List 7 Women Final.indd

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Edges a little rubbed and chipped at the crowns; back **strip** on volume one repaired; ...
Lanoe is an anagram for "alone," and Falconer a **synonym** of Hawker. ...
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[PDF] XL C Compiler Reference

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compiler. See the **strip** command in the AIX Commands Reference and the ... To compile myprogram.c using a substitute xlc **compiler** in /lib/tmp/mine/ enter: ... jumpdoc.fz-juelich.de/doc_vac_pdf/compiler.pdf

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Syn nyms:(British.) potholing;spelunking. 2. A method of mining in which the ore is ... effects such as strip mining. recorder. An instrument designed to ... www.epa.gov/ncea/pdfs/lexicon.pdf

[PDF] Editorial

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strip-mining and loop interchange on all memory hierarchy levels (cache and regis-

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Tiling: To all levels of the memory hierarchy (cache and registers) ... apply scalar

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... L2 cache (with hardware support for L2 cache prefetch) • Scalar operations ... May

also consider strip-mining at C level using. the knowledge of cache ...

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cache expensive. Finally, these applications have high computational ... strip-mining.

We would like the compiler to strip mine the input data when the ...

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64KByte direct-mapped data cache shared with the CPU. This cache has 16 byte ...

hand optimization other than induction variable analysis, strip-mining, ...

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Strip mining: generation of code such that each vector, operation is done for a size ≤

to the \dots characterize using performance monitors (cache misses, \dots

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[PDF] Lecture 7: Vector Processing

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3) tiled access to memory reduce scalar processor. memory bandwidth needs. 4) if

know max length of app. is < max vector length, no strip mining overhead ...

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IPDFI Vectorized sparse matrix multiply for compressed row storage format

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matrix multiply over the non-vectorized scalar implementation. ... ELL algorithm where

"strip-mining" is employed to reuse vector registers. The ...

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recognize reductions to both scalar and array variables. ... Second, data strip-mining

turns the 2D array into a 3D array, ...

research.compaq.com/wrl/DECarchives/DTJ/DTJT06/DTJT06PF.PDF -

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namic memory disambiguation, and strip-mining. Results ... scalar and superscalar node processors to tolerate varying. rst level memory access latency with ...

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Strip-mining is also used in order to reduce the amount of data relocated for the inner-loop, computation if that amount is too large to t in the cache. ... www.crhc.uiuc.edu/IMPACT/ftp/report/crhc-95-04.reducing.pdf - Similar pages - Filter [More results from www.crhc.uiuc.edu]

[PDF] Code Transformations to Improve Memory Parallelism

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Figure 2(c) expresses this traversal by applying strip-mine. and interchange to Figure 2(a). ... served and that scalar replacement primarily affects cache ...

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We prefer to use unroll-and-jam instead of strip-mine and interchange for two ... cache-line dependence from memory operation A to B if A is a leading ... dynamo.ecn.purdue.edu/~vpai/Publications/pai-jilp00.pdf - Similar pages - Filter [More results from dynamo.ecn.purdue.edu]

[PDF] Radix Sort and Hash-Join for Vector Computers

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mining. In strip mining an element of a vector register. handles every Lth-element ...

Mask vector is generated by a scalar-vector comparison ...

www.cag.csail.mit.edu/6.893-f2000/readslides/Vector_Sort_Hash.pdf -

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Use strip mining to minimize vector load overhead ... Avoid scalar temporary variables in a vector loop. Scalar temporaries. force unnecessary scalar stores ... www.utdallas.edu/~cantrell/ee6481/lectures/vector-opt.pdf - Similar pages - Filter

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scalar processors. Vectorization exploits regularities in the computational struc- ... ters would need the user to strip mine the innermost loop. ...

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Use of scalar types (long) rather than a struct may improve, backend code quality ...

Use performance model to strip-mine messages into ...

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[PDF] Engineer of the Year Dr. Raymond Muzic, Jr.

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Cache memory can be used to decrease the number of ... Good scalar. performance reduces "strip mining" and other overhead costs. ...

www.ewh.ieee.org/r2/cleveland/newsltr/2001/200102.pdf - Similar pages - Filter

IPDF1 16 Vector Architecture

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If vector exceeds vector register length, must use "strip mining" ... Vector chaining is

analogous to data forwarding in a scalar processor ...

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Previous work has used unroll-and-jam for scalar replacement (replacing array memory ... The shorter inner loops of strip-mining can negatively impact ... www.jilp.org/vol2/v2paper11.pdf - Similar pages - Filter

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through another half (cache line) of the L1 cach . The scalar product results are accumulated ... ant with partial pivoting and strip-mining is used [5,2] ...

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Vector instructions can be made faster than a sequence of scalar instructions, ... Strip mining overhead must also be included in start-up time computations ... www.ee.uwa.edu.au/~maceyb/aca319-2003/handouts/Section5b.pdf -Similar pages - Filter

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Subset of scalar mass routines included in the compiler runtime ... Strip-mining of vectorized loops. Vector calls to specialized routines for POWER4 (vsqrt ... www.spscicomp.org/ScicomP11/Presentations/IBM/archambault.pdf -Similar pages - Filter

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scalar and vector performance make the evaluated vector. systems particularly sensitive to ... (via strip mining) the inner grid point loop without any ... www.es.jamstec.go.jp/esc/images/jes_vol.3/pdf/JES3-41Oliker.pdf -Similar pages - Filter

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data locality - increase reuse of registers, cache ... Strip Mining is always safe. With interchange it ... Scalar replacement exposes the reuse of a(i) ...

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strip-mine and interchange is similar to. unroll-and-jam. • typically, cache architectures are more complex. • scalar replacement transformation for ... surai lums edu pk/~cs524w03/resources/data_dependences.pdf - Similar pages - Filter

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ware prefetching support with a scalar data prefetch instruction, but the lack of inline assembly support in ... Effectiveness of Message Strip-Mining for ... upc.lbl.gov/publications/x1-gas--ics04.pdf - Similar pages - Filter

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the register level, tiling only for the cache level and tiling ... Strip-mining determines the tile size in each dimension and ...

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The solution is trivial for scalar variables because a data dependence ... Tiling is legal if and only if the strip-mining and loop permutation are legal. ...

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fying assumptions have been made 100 cache hit rate, no constraints to ... limiting the

number of elements in a structure, or by strip-mining the loop. ...

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improve register/cache/TLB locality due to. shorter code length ... strip mining. do i =

1, n. a(i) = b(i) * c(i). end do. do si = 1, n, 64 ...

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FPGAs do not have built in cache. mechanisms. They are usually connected to a ...

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scalar operations on the same number of data ... Strip mining. V. When a vector has

a length greater than that of the vector ...

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sizes are smaller than the cache size. This technique is known as strip mining. In the

matrix multiplication context, this means that instead of 3 nested ...

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strip mining, each of which cannot be larger than the max-. imum amount of elements

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Memory. Vector. registers. Data. cache. Scalar FP. registers. Scalar. registers. Vload

/ VStore ... called strip mining. DO I = 1, N. A(I) = B(I) + C(I) ...

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Mining. •. Message vectorization is the optimization with highest impact ...

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tile or **strip-mining** rather than a full range of exploration on ... include one or several references to arrays (or **scalar**, since the ...

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global scalar variables (x and y) that can use the same, translation, ... strip-mining (a

single loop is transformed into a loop. nest with two loops [15]), ...

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Strip-mine and Interchange. for (..jj +=N). for(..i++). for(j=jj;j<jj+N;j++) ... Memory

operation B is cache-dependent on A if ...

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elsewhere in the code, by a scalar a thus reducing the size ... by the cache.

Strip-mine-and-interchange [2] is a transfor- ...

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ES contains a 500 MHz scalar processor with a 64KB instruction cache, ... loop and

vectorizing (via strip mining) the inner grid point loop without any user ...

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Case Study 2: Blocking (or Strip Mining). Consider this blocked code (the inner loop

should now fit in L1 cache):. DIMENSION A(N, N), B(N, N) ...

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Similar methods can be used to improve cache, performance. The key transformation.

for this, purpose is strip-mine-and-inlerchange. [Por89] ...

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access ... Strip mining: generation of code such that each vector ...

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self-product oeprator and general strip-mining. Self-product was used to ... This

project investigated the usage of a Stream Cache and its benefit in a ...

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"2D" strip-mine matrix multiplication. (tiling). • Keep working-set small so it fits in the.

cache. • Apply as many operations as possible for ...

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benefit from message strip mining; as long as the loop accesses all of its remote.

values, in general the cache miss penalties alone will provide adequate ...

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Strip mining: generation of code such that each vector. operation is done for a size ≤

to the ... characterize using performance monitors (cache misses, ...

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strip-mining and loop interchange on all memory hierarchy levels (cache and regis-

ters) to determine the tiles as usual, and, afterwards, and due to the ...

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tiling [1, 2] is a technique based on a combination of strip-. mining and loop

interchange. Loop tiling has a significant potential to remove cache. misses. ...

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Strip-mining may create more direction vectors,. ie = ==, < =< or <*, > => or >* ... (d)

Cross-strip loop is parallel. Machine has a shared cache. ...

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caches; for a shared cache, if the parallel loop is a cross-strip loop, the cache ...

Tiling sequential loops does not require balanced strip-mining. ...

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cache on most general-purpose processors. The, difference is that because there is

no cache ... so that processing elements can strip mine the data ...

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Chatterjee and Brooks [5] proposed both **strip mining** and recursive. data layout to improve the **cache** behaviour of the lifting scheme. ...

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gation) [7] and Chatterjee's method (Strip-mining, Data layout) [8], ... Cache-efficient

wavelet lifting in JPEG2000. IEEE Int. ...

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Message Strip-Mining. shared [] double *p;. float *buf;. get(buf,p,N*8);.

for(i=0;i<N;i++) Computation cost: cache miss penalties + computation time ...

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Consider a memory system where the latency to fetch a 64-byte cache block is 256ns

and the ... Use strip mining to. write DLXV code for the following loop. ...

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of distinct orderings of the loops in a nest in terms of cache lines used. ... ing

strip-mining, this value is used to divide the iteration count of the ...

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Strip-mining is also used in order to reduce the amount of data relocated for the

inner-loop, computation if that amount is too large to t in the cache. ...

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namic memory disambiguation, and strip-mining. Results ... designed as thetoplevel

cache, in high-performance parallel. systems. ...

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To further reduce the data cache misses, the strip mining optimization is applied to

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Optimization for Cache Memory. Strip-mine directive. Strip-mine directive. Array A.

*SOPTION STRIPMINING_START. DO J=1,N. DO I=L, N ... = A(I,J) ...

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***Not advantageous for machines w/cache ... mining. In strip mining an element of

a vector register. handles very Lth-element ...

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ELL algorithm where "strip-mining" is employed to reuse vector registers. The ... back

L2 cache. Memory bandwidth is 34.1 GBytes/s from memory to cache and ...

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Avoid cache misses. Avoid cache misses. Reduce the working data set. Reduce the

working data set. Use Strip. Use Strip--mining to transform a large data ...

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direct approach, but the strip size may be used to deter-. mine the amount of data

loaded into the cache for each. array referenced in the inner loops. ...

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A vector of arbitrary length processed with strip mining. m = n mod MVL ... Tiling is

an extension of strip-mining to multi-dimentional ...

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The tranpose subdirectory explores cache thrashing by examining array index order,

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Strip mining. • Strip mining can be extended over the whole code! see domain

decomposition. But: Generally only a fraction of the cach (1/4) can be used ...

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The process is called strip-mining. The strip- ... cach sizes. Sweep accesses of a

large vector may result in complete reloading of ...

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Triangular Cache Blocking. •. Applying strip mining. DO I = 2, N, K. DO ii = I, I+K-1.

DO J = 1, ii - 1. A(ii, J) = A(ii, ii) + A(J, J) ...

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Figure 2(c) expresses this traversal by applying strip-mine ... tive for memory

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unrolling, cache tiling/strip-mining, interchange etc.) on steroids, and is a big win for.

every architecture but a huge win on IA64 due to its big caches ...

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we'll cache for another column). To someone untrained in acting ... Strip-mining,

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Use performance model to strip-mine messages into ... 4 SSP slightly better than 1

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data locality - increase reuse of registers, cache. • parallelism - eliminate

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improve register/cache/TLB locality due to. shorter code length ... strip mining. do i =

1, n. a(i) = b(i) * c(i). end do. do si = 1, n, 64 ...

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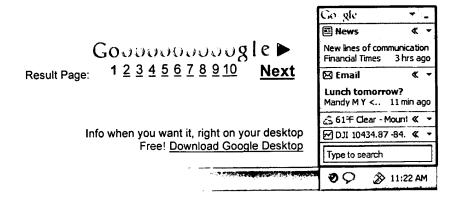
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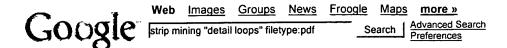
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one cache level resides on-chip. The transfer and storage organization is left ... loop

transformations are the loop merging, loop interchange, strip mining ...

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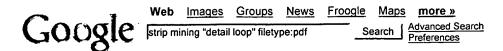
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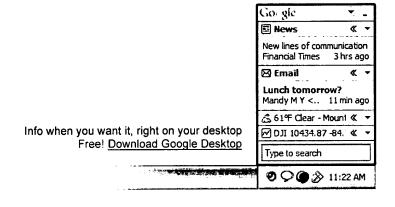
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